IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:
Vaidyanathan, et. al.

Serial No.: 10/682,387

Filed: 10/9/2003

Title: System, Apparatus and Method of Enhancing Priority Boosting of Scheduled Threads

§ **Confirmation No. 8372**
§ **Notice of Allowance**
§
§ **Examiner: Syed, Farhan M.**
§
§ **Intellectual Property Law Department**
§ **International Business**
§ **Machines Corporation**
§ **11400 Burnet Road**
§ **Austin, Texas 78758**
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Date: November 3, 2009

Respectfully submitted,

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